

In the Claims

Amend claims 1, 10 and 16, and cancel claims 2, 11 and 17 as follows::

1. (currently amended) A method of creating a photomask layout for projecting an image of an integrated circuit design comprising:

creating a layout of spaced integrated circuit shapes to be projected via the photomask;

creating Voronoi cells around the spaced integrated circuit shapes;

determining bisectors between adjacent ones of the spaced integrated circuit shapes, the bisectors comprising locus of points equidistant from edges of the adjacent spaced integrated circuit shapes and defining shared boundaries of adjacent Voronoi cells; and

creating sub-resolution assist features along at least some of the bisectors between the adjacent ones of the spaced integrated circuit shapes.

2. (cancelled)

3. (original) The method of claim 1 wherein the adjacent ones of the spaced integrated circuit shapes are parallel to each other and the sub-resolution assist features along the bisectors are parallel to the spaced integrated circuit shapes.

4. (original) The method of claim 1 further including identifying different types of vertices for the bisectors prior to creating the sub-resolution assist features, and

prioritizing creation of the sub-resolution assist features in accordance with the type of vertex.

5. (original) The method of claim 1 further including extending at least some of the sub-resolution assist features beyond the bisectors on which they are created.

6. (original) The method of claim 1 further including extending at least some of the sub-resolution assist features beyond the bisectors on which they are created to connect to other sub-resolution assist features.

7. (original) The method of claim 1 further including removing at least one of the sub-resolution assist features along the bisectors prior to finalizing the photomask layout.

8. (original) The method of claim 1 wherein the integrated circuit shapes are two-dimensional and include shapes having edges parallel and perpendicular to each other, between which the bisectors are located.

9. (original) The method of claim 1 wherein the integrated circuit shapes are two-dimensional and include shapes having lengths of parallel edges in which an edge of one shape ends at a point within the length of the other shape, between which the bisectors are located.

10. (currently amended) A program storage device readable by a machine, tangibly embodying a program of instructions executable by the machine to perform method steps for creating a photomask layout for projecting an image of an integrated circuit design, said method steps comprising:

creating a layout of spaced integrated circuit shapes to be projected via the

photomask;

creating Voronoi cells around the spaced integrated circuit shapes;

determining bisectors between adjacent ones of the spaced integrated circuit

shapes, the bisectors comprising locus of points equidistant from edges of

the adjacent spaced integrated circuit shapes and defining shared

boundaries of adjacent Voronoi cells; and

creating sub-resolution assist features along at least some of the bisectors

between the adjacent ones of the spaced integrated circuit shapes.

11. (cancelled)

12. (original) The program storage device of claim 10 wherein, in the method, the adjacent ones of the spaced integrated circuit shapes are parallel to each other and the sub-resolution assist features along the bisectors are parallel to the spaced integrated circuit shapes.

13. (original) The program storage device of claim 10 further including, in the method, identifying different types of vertices for the bisectors prior to creating the sub-

resolution assist features, and prioritizing creation of the sub-resolution assist features in accordance with the type of vertex.

14. (original) The program storage device of claim 10 further including, in the method, extending at least some of the sub-resolution assist features beyond the bisectors on which they are created.

15. (original) The program storage device of claim 10 further including, in the method, extending at least some of the sub-resolution assist features beyond the bisectors on which they are created to connect to other sub-resolution assist features.

16. (currently amended) An article of manufacture comprising a computer-readable medium having computer readable program code means embodied therein for creating a photomask layout for projecting an image of an integrated circuit design, the computer readable program code means in said article of manufacture comprising:

computer readable program code means for creating a layout of spaced integrated circuit shapes to be projected via the photomask;
computer readable program code means for creating Voronoi cells around the spaced integrated circuit shapes;

computer readable program code means for determining bisectors between adjacent ones of the spaced integrated circuit shapes, the bisectors comprising locus of points equidistant from edges of the adjacent spaced integrated circuit shapes and defining shared boundaries of adjacent Voronoi cells; and

computer readable program code means for creating sub-resolution assist features along at least some of the bisectors between the adjacent ones of the spaced integrated circuit shapes.

17. (cancelled)
18. (original) The article of manufacture of claim 16 wherein the adjacent ones of the spaced integrated circuit shapes are parallel to each other and the sub-resolution assist features along the bisectors are parallel to the spaced integrated circuit shapes.
19. (original) The article of manufacture of claim 16 wherein the computer readable program code means in said article of manufacture further includes computer readable program code means for identifying different types of vertices for the bisectors prior to creating the sub-resolution assist features, and prioritizing creation of the sub-resolution assist features in accordance with the type of vertex.
20. (original) The article of manufacture of claim 16 wherein the computer readable program code means in said article of manufacture further includes computer readable program code means for extending at least some of the sub-resolution assist features beyond the bisectors on which they are created.